Abstracts

An isolated-open pattern to de-embed pad parasitics [CMOSFETs]

Chung-Hwan Kim, Cheon Soo Kim, Hyun Kyu Yu and Kee Soo Nam. "An isolated-open pattern to de-embed pad parasitics [CMOSFETs]." 1998 Microwave and Guided Wave Letters 8.2 (Feb. 1998 [MGWL]): 96-98.

To meet radio frequency (RF) performance required in large market of wireless applications, CMOS transistors having a small unit gate width are preferred. To correctly estimate RF performance, parasitics of the on-wafer pads and interconnection metal lines should be de-embedded as in the advanced bipolar transistors. However, cutoff frequencies of small-size MOSFETs de-embedded by the conventional on-wafer dummy structures result in large overestimation. A new open pattern is proposed to solve the problem. The meaning and justification of the new de-embedding pattern are discussed.

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